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S/N 09/964,010PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Matthew B. Haycock et al.	Examiner:	Tse Chen
Serial No.:	09/964,010	Group Art Unit:	2116
Filed:	September 26, 2001	Docket No:	884.455US1
Title	METHOD AND APPARATUS FOR REALIGNING BITS ON A PARALLEL BUS		
Assignee:	Intel Corporation	Customer Number:	21186

PETITION FOR A ONE-MONTH EXTENSION OF TIME

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed July 29, 2005, said period of response being extended from October 29, 2005 to November 29, 2005.

Please charge Deposit Account No. 19-0743 in the amount of \$120.00 to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.

Respectfully Submitted

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Date: 29 November 2005 By: \_\_\_\_\_

  
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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

  
Amy J. Moriarty

November 29, 2005  
Date of Transmission